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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/672,937	09/28/2000	Kevin A. Retlich	00AB188	7594	
7:	590 10/08/2003		EXAM	INER	
John J Horn			DESTA,	ELIAS	
Allen-Bradley Patent Dept 704			ART UNIT	PAPER NUMBER	
1201 South Second Street		2857			
Milwaukee, W	I 53204-2496	•	DATE MAILED: 10/08/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

			Application	on No.	Applicant(s)	(m)		
•				37	RETLICH ET AL.			
		Office Action Summ ry	Examiner		Art Unit	<del></del>		
•			Elias Des	ta	2857			
	Period for	Th MAILING DATE of this communication Reply	appears on the	cover sheet with th	correspondence ad	dress		
	THE M - Extens after S - If the p - If NO p - Failure - Any re	PRTENED STATUTORY PERIOD FOR RE AILING DATE OF THIS COMMUNICATIO ions of time may be available under the provisions of 37 CFI X(6) MONTHS from the mailing date of this communication eriod for reply specified above is less than thirty (30) days, a period for reply is specified above, the maximum statutory per to reply within the set or extended period for reply will, by stoly received by the Office later than three months after the me patent term adjustment. See 37 CFR 1.704(b).	N. R 1.136(a). In no eve . reply within the statu riod will apply and wi atute, cause the appl	ent, however, may a reply be tin story minimum of thirty (30) day Il expire SIX (6) MONTHS from ication to become ABANDONE	nely filed s will be considered timely the mailing date of this co D (35 U.S.C. § 133).			
	l	Responsive to communication(s) filed on	15 August 200:	3				
	1		This action is					
	3)□	Since this application is in condition for all	owance excep	t for formal matters, p		e merits is		
	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213. <b>Disposition of Claims</b>							
	4) 🖂 (	Claim(s) $1-33$ is/are pending in the applica	tion.					
	4	a) Of the above claim(s) is/are with	drawn from coi	nsideration.				
	5)⊠ (	Claim(s) <u>13-33</u> is/are allowed.						
	6)⊠ (	Claim(s) <u>1,2,5,8,10 and 12</u> is/are rejected.						
	7)🛛 (	Claim(s) <u>3,4,6,7,9 and 11</u> is/are objected to	) <b>.</b>					
	8) Claim(s) are subject to restriction and/or election requirement.							
	Application Papers							
	9)□ T	he specification is objected to by the Exam	niner.					
	10)⊠ Ti	he drawing(s) filed on <u>28 September 2000</u>	is/are: a)□ ac	cepted or b) abjected	to by the Examine	r.		
ENCY HE THE PROPERTY.	į	Applicant may not request that any objection to						
	11)∐ TI	he proposed drawing correction filed on			ved by the Examine	er.		
	If approved, corrected drawings are required in reply to this Office action.							
		12)☐ The oath or declaration is objected to by the Examiner.						
	Priority under 35 U.S.C. §§ 119 and 120							
	13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
	a)_	All b) Some * c) None of:						
	1	. Certified copies of the priority docum	ents have bee	n received.				
	2	2. Certified copies of the priority docum	ents have bee	n received in Applicati	on No			
		Copies of the certified copies of the paper application from the International te the attached detailed Office action for a	Bureau (PCT	Rule 17.2(a)).		Stage		
		knowledgment is made of a claim for dom				application)		
	a)	☐ The translation of the foreign language	provisional ap	plication has been rec	eived.			
	15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.  Attachment(s)							
	1) Notice	of References Cited (PTO-892) of Draftsperson's Patent Drawing Review (PTO-948) ation Disclosure Statement(s) (PTO-1449) Paper No(		·	/ (PTO-413) Paper No( Patent Application (PTC			

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### **Detailed Action**

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### Drawing

- 1. The drawing is objected to because of the following minor informalities:
  - Figs. 2 and 3: member 80 points at two distinct structures where member 82 also refers to part of the same embodiment, preferably member 80 should refer to "sector" and member 82 to the "block".
  - ➤ Fig. 2: member 84 is similar in structure as member 86 in Fig. 3, whereas in Fig. 2, include an Input/Output port with the sensor configuration for better clarity.

## Claim rejection - 35 U.S.C. 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) The invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. <u>Claims 1, 2, 5, 8, 10 and 12</u> are rejected under 35 U.S.C. 102(b) as anticipated by Helf, Jr. et al. (U.S. Patent 3,764,995).

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In reference to claim 1: *Helf, Jr. et al.* (U.S. Patent 3,764,995) teaches a method for monitoring operational parameters of a system of electrical components (devices or units under test (UUT)) (see *Helf, Jr. et al.*, Fig. 2 and column 1, line 50 to column 2, line 21). The method includes:

- Storing in a memory circuit of each component identity data respective of an identity of the respective component in the system (see *Helf, Jr. et al.*, column 2, lines 28-35 and column 5, lines 4-27);
- ➤ Sensing operational parameters (stimuli) of each component and processing the sensed parameters in the respective component (see *Helf, Jr. et al.*, column 2, lines 53-66);
- respective component (Unit Under Test) back to the monitoring station (computer) (see <u>Helf, Jr. et al.</u>, Fig. 2, Test data to computer station)
- ➤ Generating a user viewable monitoring display of the parameters by component based upon the sensed parameters and identity data (see *Helf, Jr. et al.*, column 4, lines 25–32).

With regard to claim 2, as noted above in claim 1, <u>Helf, Jr. et al.</u> further includes that the identity data represent the node address of the component

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because in order for the computer to identify the port associated with the Unit Under Test, it has to have a node address (see <u>Helf, Jr. et al.</u>, Fig. 2, response and stimulus signals are addressable).

With regard to claim 5: as noted above in claim 1, Helf, Jr. et al. further teaches that the parameter is selected based on the identity data (see Helf, Jr. et al., column 2, lines 53-66).

With regard to claim 8, as noted above in claim 1, <u>Helf, Jr. et al</u>. further includes that a textual display of operating parameter of the component or Unit Under Test (see <u>Helf, Jr. et al</u>., column 4, lines 25–32 and column 20, lines 16–19, teletypewriter is a text processing output).

With regard to claim 10: as noted above in claim 1, <u>Helf, Jr. et al.</u> further teaches that the monitoring station (Fig. 2, computer station) accesses a database for the system to obtain data descriptive of the components, and the monitoring display includes the description of the respective components (see <u>Helf, Jr. et al.</u>, column 3, lines 2-10).

With regard to claim 12: as noted above in claim 10, *Helf, Jr. et al.* further teaches that the description includes a textual description of the respective component (see *Helf, Jr. et al.*, column 4, lines 25–32 and column 20, lines 16–19, teletypewriter is a text processing output).

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### Allowable Subject Matter

4. <u>Claims 3, 4, 6, 7, 9 and 11</u> are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

#### Allowance

- 5. <u>In reference to claim 13-33</u>: <u>Helf, Jr. et al.</u> (U.S. Patent 3,764,995) teaches a method for monitoring operational parameters of a system of electrical components (devices or units under test (UUT)) (see <u>Helf, Jr. et al.</u>, Fig. 2 and column 1, line 50 to column 2, line 21). The method includes:
  - Storing in a memory circuit of each component identity data

    respective of an identity of the respective component in the system

    (see <u>Helf, Jr. et al.</u>, column 2, lines 28–35 and column 5, lines 4–27);
    - ➤ Sensing operational parameters (stimuli) of each component and processing the sensed parameters in the respective component (see *Helf, Jr. et al.*, column 2, lines 53-66);
    - > Transmitting the sensed parameters and the identity data of the respective component (Unit Under Test) back to the monitoring

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station (computer) (see <u>Helf, Jr. et al</u>., Fig. 2, Test data to computer station)

➤ Generating a user viewable monitoring display of the parameters by component based upon the sensed parameters and identity data (see *Helf, Jr. et al.*, column 4, lines 25-32).

As for testing or monitoring the devices over the network, <u>Helf, Jr. et al.</u> further shows that the monitoring apparatus or computer is also networked to test or monitor the electronic component or device from a remote site (see <u>Helf, Jr. et al.</u>, column 2, lines 3–5 and column 7, lines 30–56).

However, <u>Helf, Jr. et al</u>. does not teach a viewable representation of the devices or physical layout data of individual components.

As noted above, the claimed invention provides a method for monitoring operational parameters of a system of electrical components. Further, the method includes generating a series of user viewable representation of operational parameter based on the physical layout of each component.

The remaining claims are dependent upon <u>claims 13, 22 and 28</u> and contain further limitations.

The <u>prior art</u> made of record and not relied upon is considered pertinent to applicant disclosure.

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- Parulkar et al. (IEEE Article, "An Architecture for Monitoring, Visualization and Control of Gigabit Networks") teaches a case study of NMVC system with advanced algorithmic human-in-the-loop capability.
- Goncharenko et al. (University of Tokyo, white paper) describes an information-centered approach to maintenance based on criteria for product life cycle optimization.
- Dpaonthenet (Editorial Extra from Dpaonthenet.net) describes an available technology that used by London Electric Services uses transparent ready system for managing substations from a remote location.
- \* Burkhard (U.S. Patent 6,574,652) teaches intrinsically safe

  communication and control system for use in hazardous locations

  including monitoring device with intrinsically safe fluorescent tube

  backlit.
- Fredriksson (U.S. Patent 6,000,825) teaches a method and arrangement for a module, which can be connected to a serial, and digital network system.

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▶ <u>Davis et al.</u> (U.S. PAP 2001/0056483 A1) teaches the method and apparatus for monitoring a computer system with system management controller.

Eidson et al. (U.S. Patent 5,586,305) teaches smart distributed measurement and control system with a flexible architecture.

#### Conclusion

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Elias Desta whose telephone number is (703)-305-3840. The examiner can normally be reached on M-F (8:00-4:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Marc S. Hoff can be reached on (703)–308–1677. The fax phone numbers for the organization where this application or proceeding is assigned are (703)–308–5841 for regular communications and (703)–308–5841 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703)-308-1782.

Elias Desta Examiner Art Unit 2857

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800

Secure   Text	
1985   52	Time Stamp
EAST 12   19   19   19   19   19   19   19	09/16/03
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Sec.   1	09/16/03
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BRS   36   (145619 or 34652 or 346533 or 34567 or 345672 or 345672 or 345672 or 345672 or 345672 or 345673 or 345672 or 345747 or 34574 or 3457	09/16/03
SPATE   SPAT	09/16/03
SRS   7	09/16/03
Service   Serv	
Section	09/16/03
Section   Components   Section	09/16/03
SRS   16-05	09/16/03
BRS   1E-46	09/16/03
BRS   1953   ((((e)ectrical with components) and (stor5d same memory)) and (sens35 same parameters)   USPAT, USPGPUB, EPC, IPC) (DERWENT; IBM, TDB   BRS   2074   ((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters)) and ((transmitting with data)   USPAT, USPGPUB, EPC, IPC) (DERWENT; IBM, TDB   ((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters)) and (transmitting with data)   USPAT, USPGPUB, EPC, IPC) (DERWENT; IBM, TDB   (((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters)) and (transmitting with data)   USPAT, USPGPUB, EPC, IPC) (DERWENT; IBM, TDB   ((((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   ((((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   (((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   (((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   (((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   (((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   (((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   (((((e)ectrical with components) and (stor3d same memory)) and (sens35 same parameters))   (((((e)ectrical with components) and (stor3d same memory))   (((((e)ectrical with components) and (stor3d same memory))   ((((e)ectrical with components)   (((e)ectrical with components)   (((e)ectrical with co	09/16/03
BRS   547	
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BRS   0   ((((((((((((((((((((((((((((((((	09/16/03
BRS   3477   (electrical adj circuit) and monitoring   USPAT, US-PGPUB, EPO_JPO, DERWENT; IBM, TDB   BRS   5887   ((electrical adj circuit) and monitoring) and storing   USPAT, US-PGPUB, EPO_JPO, DERWENT; IBM, TDB   BRS   5881   ((electrical adj circuit) and monitoring) and storing   USPAT, US-PGPUB, EPO_JPO, DERWENT; IBM, TDB   BRS   5881   ((electrical adj circuit) and monitoring) and storing   USPAT, US-PGPUB, EPO_JPO, DERWENT; IBM, TDB   BRS   5881   (((electrical adj circuit) and monitoring) and storing   USPAT, US-PGPUB, EPO_JPO, DERWENT; IBM, TDB   BRS   4812   ((((electrical adj circuit) and monitoring) and storing) and sensing) and transmitting   USPAT, US-PGPUB, EPO_JPO, DERWENT; IBM, TDB   C(((((electrical adj circuit)) and monitoring) and storing) and sensing) and transmitting   USPAT, US-PGPUB, EPO_JPO, DERWENT; IBM, TDB   C(((((((((((((((((((((((((((((((((((	09/16/03
BRS   1477	09/16/03
ERS   5887	09/16/03
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SFS   SFS	09/16/03
RSS   4855   ((((((electrical and circuit) and monitoring) and storing) and sensing) and transmitting) and generating   USPAT; USPGPUB; EPO; JPO; DERWENT; IBM, TDB	09/16/03
BRS   21   ((((((((((((((((((((((((((((((((	09/16/03
BRS   1472   (702/179 or 702/183 or 702/185 or 702/18	
RRS   376   (70277-3-74 or 70280 or 702/119-124).cds.   USPAT; US-PGPUB; EPO, JPO, DERWENT; IBM, TDB   RRS   1641   (702/10-10-10-10-10-10-13-4 or 702/10-13-13-0 or 702/10-17-10-10-10-10-10-10-10-10-10-10-10-10-10-	09/16/03
RRS   976   (70277-3-74 or 702/80 or 702/119-124).ccls   USPAT; US-PGPUB; EPO, JPO, DERWENT; IBM, TDB   RRS   1581   (702/10-103-10-104 or 702/10-1134 or 702/10-133 or 702/10-13-124).ccls   USPAT; US-PGPUB; EPO, JPO, DERWENT; IBM, TDB   (702/10-103-10-104 or 702/10-1134 or 702/10-13-13 or 702/10-13-124).ccls   USPAT; US-PGPUB; EPO, JPO, DERWENT; IBM, TDB   (702/10-103-10-104 or 702/10-13-13-0) or (702/10-13-0) or 702/10-13-0 or 702/10-13-0   USPAT; US-PGPUB; EPO, JPO, DERWENT; IBM, TDB   (702/10-13-0-10-14-0) or 702/10-13-0	09/16/03
BRS   1298   (702/57-59 or 702/66-71)ccds.   USPAT; US-PGPUB; EPO, JPO; DERWENT; IBM_TDB   USPAT; US-PGPUB; EPO,	09/16/03
FRS   1641	09/16/03
RRS   4842   4	09/16/03
T02/for.171).cds.   T02/for.172   T02/for.172   T02/for.173   T02/for.	09/16/03
Secondary   Seco	
Component   Comp	09/16/03
702/for.171).ccls.)) and (electric adj circuit)) and component  ((((((702/179 or 702/183 or 702/187 or 702/187).ccls.) or ((702/73-74 or 702/80 or 702/19-124).ccls.) or ((702/57-59 or 702/66-71).ccls.) and (electric adj circuit)) and component) and stor\$3  ((((((702/179 or 702/183 or 702/183 or 702/187 or 702/189).ccls.) or ((702/73-74 or 702/80 or 702/19-124).ccls.) or ((702/57-59 or 702/66-71).ccls.)) and (electric adj circuit)) and component) and stor\$3  BRS 26 (((((702/179 or 702/183 or 702/187 or 702/189).ccls.) or ((702/73-74 or 702/80 or 702/19-124).ccls.) or ((702/17-11).ccls.)) and (electric adj circuit)) and component) and stor\$3) and sen\$3  (((((((702/179 or 702/183 or 702/185 or 702/187 or 702/189).ccls.) or ((702/73-74 or 702/80 or 702/19-124).ccls.) or ((((((((702/179 or 702/183) cr.104 or 702/16r.110 or 702/16r.134 or 702/16r.139 or 702/16r.170 or 702/16r.171).ccls.)) and (electric adj circuit)) and component) and stor\$3) and sen\$3) and transmit\$4  ((((((((((((702/179 or 702/183 or 702/185 or 702/187 or 702/189).ccls.) or ((702/73-74 or 702/80 or 702/19-124).ccls.) or ((702/73-75 or 702/183 or 702/185 or 702/187 or 702/183).ccls.) or ((702/73-74 or 702/80 or 702/19-124).ccls.) or ((702/73-75 or 702/183 or 702/185 or 702/185 or 702/185).ccls.) or ((702/73-74 or 702/80 or 702/19-124).ccls.) or ((702/73-75 or 702/66-71).ccls.)) and (electric adj circuit)) and component) and stor\$3) and sen\$3) and transmit\$4 or 702/10r.170 or 702/10r.170 or 702/10r.171).ccls.)) and (electric adj circuit)) and component) and stor\$3) and sen\$3) and transmit\$4) and generat\$3  (((((((((((702/179 or 702/183 or 702/185 or 7	
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T02/for.171/ccls.) and (electric adj circuit)) and component) and stor\$3	09/16/03
BRS   26   ((702/57-59 or 702/66-71).ccls.) or ((702/for.103-for.104 or 702/for.134 or 702/for.139 or 702/for.170 or 702/for.171).ccls.) and (electric adj. circuit)) and component) and stor\$3) and sen\$3   (((((((702/75-59 or 702/65-71).ccls.) or ((702/75-759 or 702/75-759 or 702/75-759 or 702/75-75-702/75-700 or (702/75-759 or 702/75-75-702/75-702/75-700 or (702/75-759 or 702/75-75-702/75-700 or (702/75-759 or 702/75-700 or (702/75-759 or 702/75-75-702/75-700 or (702/75-759 or 702/75-75-702/75-700 or (702/75-759 or 702/75	33/10/03
BRS   13   ((702/57-59 or 702/66-71),ccls.) or ((702/for.103-for.104 or 702/for.131 or 702/for.139 or 702/for.170 or 702/for.171),ccls.)) and (electric adj circuit)) and component) and stor\$3) and transmit\$4   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   (((((((((((((((((((((((((((((((((((	09/16/03
BRS   12   ((702/57-59 or 702/66-71).ccls.) or ((702/for.103-for.104 or 702/for.130 or 702/for.139 or 702/for.170 or 702/for.171).ccls.)) and (electric adj circuit)) and component) and stor\$3) and sen\$3) and transmit\$4) and generat\$3   USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   (((((((((((((((((((((((((((((((((((	09/16/03
BRS         7022         Circuit adj monitoring)         USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB           BRS         18         (circuit adj monitoring)         (circuit adj monitoring)         USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB           BRS         14         (circuit adj monitoring)         and operation adj parameters)         USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB           USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB         USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB           USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB         USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS     18     (circuit adj monitoring) and (operation adj parameters)     USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB       BRS     14     ((circuit adj monitoring) and (operation adj parameters)) and stor\$3     USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS     18     (circuit adj monitoring) and (operation adj parameters)     USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB       BRS     14     ((circuit adj monitoring) and (operation adj parameters)) and stor\$3     USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
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BRS 11 (((circuit adj monitoring) and (operation adj parameters)) and stor\$3) and sens\$3 USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03

Type	Hits	Search Text	DBs	Time Stamp
BRS	6	((((circuit adj monitoring) and (operation adj parameters)) and stor\$3) and sens\$3) and generating	USPAT: US-PGPUB: EPO: JPO: DERWENT; IBM_TDB	09/16/03
BRS	11	(((circuit adj monitoring) and (operation adj parameters)) and stor\$3) and sens\$3) and generat\$3	USPAT: US-PGPUB: EPO: JPO: DERWENT; IBM_TDB	09/16/03
IS&R	1040	(702/188).CCLS.	USPAT: US-PGPUB: EPO: JPO: DERWENT: IBM_TDB	09/16/03
BRS	7134	(700/9-17 or 700/28-31 or 700/108 or 700/245 or 700/254).ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	263	((700/9-17 or 700/28-31 or 700/108 or 700/245 or 700/254).ccls.) and (electrical same components)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	184	(((700/9-17 or 700/28-31 or 700/108 or 700/245 or 700/254).ccls.) and (electrical same components)) and monitor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	17	((((700/9-17 or 700/28-31 or 700/108 or 700/245 or 700/254).ccls.) and (electrical same components)) and monitor\$3) and (identity same data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	17	(((((700/9-17 or 700/28-31 or 700/108 or 700/245 or 700/254).cols.) and (electrical same components)) and monitor\$3) and (identity same data)) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
IS&R	2	(*5586305*).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	1	(("5586305").PN.) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	0	((("5586305").PN.) and memory) and transmitting	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	1	((((((345/619 or 345/632 or 345/633 or 345/667 or 345/672 or 345/740-741).ccls.) and (electrical adj components)) and memory) and sensing) and transmitting) and generating) and monitoring	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	2	((((((electrical with components) and (stor\$3 same memory)) and (sens\$3 same parameter\$1)) and (transmitting with data)) and (monitor\$3 same display)) and (physical adi layout)) and graphical	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	7	(((((electrical with components) and (stor\$3 same memory)) and (sens\$3 same parameter\$1)) and (transmitting with data)) and (monitor\$3 same display)) and (physical adj layout)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	11	electrical adj components same remote with monitoring adj system	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	21	(((((electrical adj circuit) and monitoring) and storing) and sensing) and transmitting) and generating) and (physical adj layout)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	21	((((((((electrical adj circuit) and monitoring) and storing) and sensing) and transmitting) and generating) and (physical adj	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	5	(((((circuit adj monitoring) and (operation adj parameters)) and stor\$3) and sens\$3) and generat\$3) and transmit\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	11	electrical adj components same remote with monitoring adj system	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	3	((((((((((((((((((((((((((((((((((((((	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	1	((((700/9-17 or 700/28-31 or 700/108 or 700/245 or 700/254).ccls.) and (electrical same components)) and monitor\$3) and (identity adj data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	15	((((((700/9-17 or 700/28-31 or 700/108 or 700/245 or 700/254).ccls.) and (electrical same components)) and monitor\$3) and (identity same data)) and memory) and parameter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	1	(("5586305").PN.) and memory	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03
BRS	1	((("5586305").PN.) and memory) and transmit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	09/16/03